

IDT-1616

SN: 09/887,913

IN THE UNITED STATES PATENT AND TRADEMARK OFFICERECEIVED  
CENTRAL FAX CENTER  
OCT 18 2004

Inventor: Jeffrey Lukanc

Assignee: Integrated Device Technology, Inc.

Title: TEST BUS ARCHITECTURE FOR EMBEDDED RAM AND METHOD  
OF OPERATING SAME

Serial No: 09/887,913

Group No.: 2133

Filed: 6/20/2001

Examiner: Guy Lamarre

Attorney Docket No: IDT-1616

Date: October 18, 2004

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Mail Stop Amendment

Commissioner of Patents and Trademarks

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AMENDMENT IN RESPONSE TO SECOND OFFICE ACTION

Sir:

Applicant submits the following amendments and remarks in response to the Office Action dated June 17, 2004. A Request for Extension of Time, extending the period for response to October 18, 2004, accompanies the present response (October 17<sup>th</sup> is a Sunday).